

WE CLAIM:

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1. A wireless receiver comprising:

a receiver circuit that receives a wireless signal;

a demodulator circuit coupled to the receiver circuit,
the demodulator circuit recovering a data signal and an first
clock signal from the signal output by the receiver circuit;

10 a first-in first-out memory (FIFO) coupled to the
demodulator circuit to receive the data signal and the first
clock signal, wherein the FIFO stores the data signal in
synchronization with the first clock signal.

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